

Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1. (Currently amended) A method of testing digital graphics data, the method comprising the steps of:

providing digital graphics data having an expected characteristic value, to a [[video]] digital graphics port of a graphics controller under test and which is coupled to a computer via a bus;

receiving, at a test apparatus, the digital graphics data from a ~~graphics output port~~ the digital graphics port of the graphics controller under test;

calculating at the test apparatus, a characteristic value that is based upon the digital graphics data; and

sending the calculated characteristic from a serial data port of the test apparatus to a serial data interface of the digital graphics port of the graphics controller.

2. (Original) The method of claim 1, wherein the expected characteristic is a calculated value based upon the predetermined type of digital graphics data.

3. (Original) The method of claim 2, wherein the predetermined type of digital graphics data includes at least one of a red, green, and blue color component.

4. (Canceled)

5. (Canceled)

6. (Canceled)

7. (Original) The method of claim 2, wherein the expected characteristic is a circular redundancy check (CRC) value.

8. (Original) The method of claim 1, wherein the predetermined type of digital graphics data is selectable.
9. (Previously presented) The method of claim 1, wherein the step of providing the calculated characteristic from the test apparatus to a serial interface includes providing the calculated characteristic to the computer; and the step of comparing the calculated characteristic to the expected characteristic by the graphics controller is replaced by the step of: comparing the calculated characteristic to the expected characteristic by at least one of: the computer and the graphics controller.
10. (Previously presented) The method of claim 1, wherein the step of receiving, at a test apparatus includes the step of: receiving at a test apparatus, the graphics data at a rate greater than 100 MHz; and the steps of calculating at the test apparatus, and the step of providing the calculated characteristic, are performed in real time with respect to the step of receiving at a test apparatus.
11. (Canceled)
12. (Original) The method of claim 1, wherein the graphics output port includes an output port for a flat panel display.
13. (Original) The method of claim 1, wherein the serial interface is associated with the graphics output port.
14. (Previously presented) A method of testing digital graphics data, the method comprising the steps of:
 - receiving digital graphics data at a graphics port of graphics test apparatus, said graphics port having a serial data interface;
 - determining a characteristic value upon the reception of the digital graphics data at said graphics test apparatus; and
 - transferring the characteristic value from the graphics test apparatus to a graphics controller over the serial data interface of the graphics port of the graphics test apparatus.

15. (Original) The method of claim 14, wherein the step of providing includes the graphics port being part of a digital graphics interconnect port.
16. (Original) The method of claim 15, wherein the digital graphics interconnect is based on a Digital Flat Panel interconnect standard interconnect.
17. (Original) The method of claim 14, wherein the steps of determining and providing occur in real-time with respect to the step of receiving.
18. (Original) The method of claim 17, wherein the step of receiving includes receiving graphics data at a clock rate of at least 100 MHz.
19. (Currently amended) An apparatus for testing digital graphics data, the system comprising:
- a connector to ~~interface to a digital graphics port of a graphics controller~~ receive the digital graphics data sent from a graphics controller;
- a graphics data analyzer module having an input coupled to the connector, and an output, said graphics data analyzer being capable of calculating a value from digital graphics data it receives through the connector; and
- a serial bus interface control module having an input coupled to the output of the graphics data analyzer module, and a serial data port coupled to the connector, said serial bus interface control module being capable of sending serial data from said serial data port.
20. (Original) The system of claim 19, wherein the serial data port is coupled to the connector to transmit serial data based upon the digital graphics protocol.
21. (Original) The system of claim 20, wherein the digital graphics protocol is a Digital Flat Panel standard.
22. (Original) The system of claim 19, further comprising a power supply terminal to receive power from a peripheral component interface (PCI) bus.
23. (Canceled)

24. (Canceled)